

FIG. 1

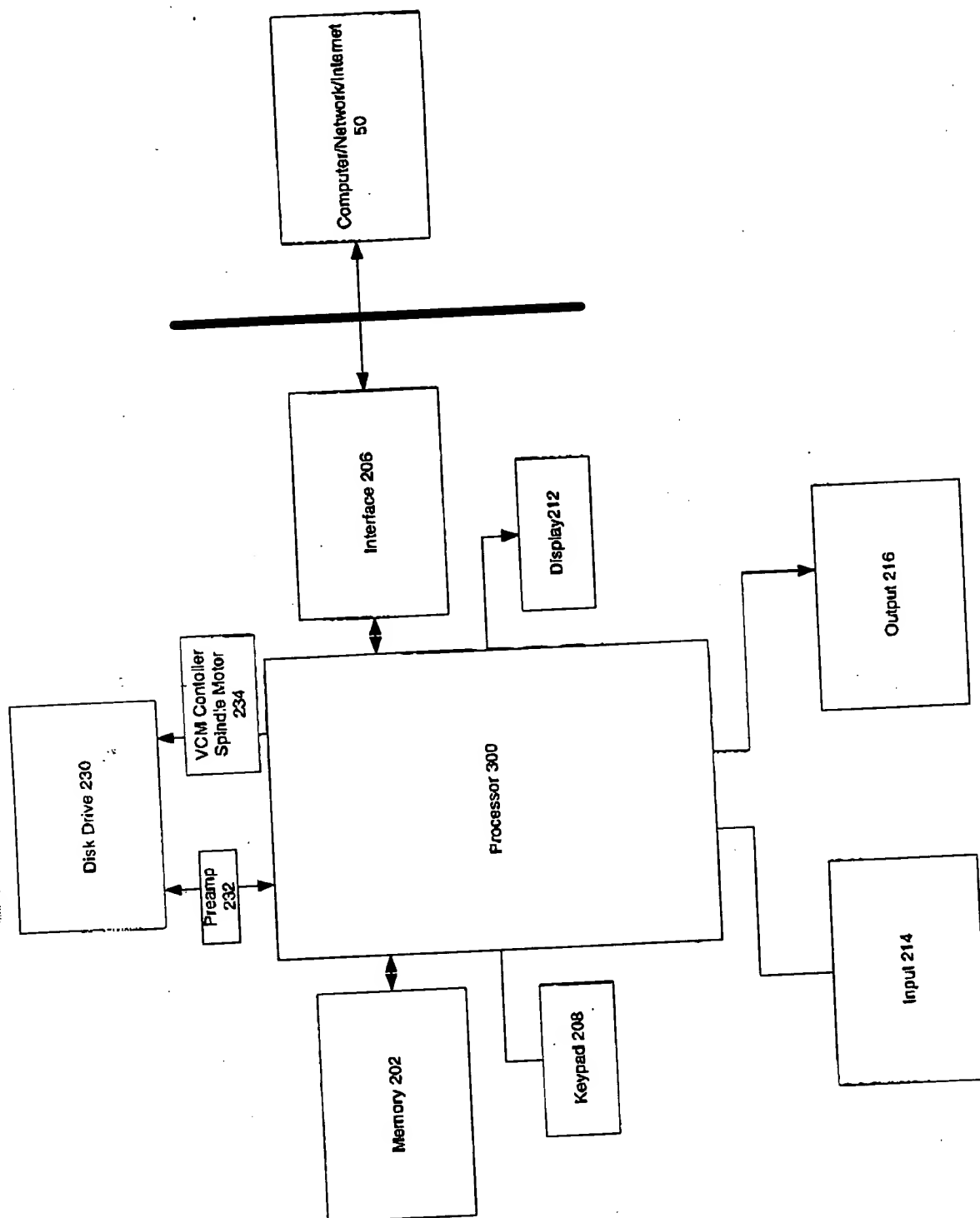


FIG. 2

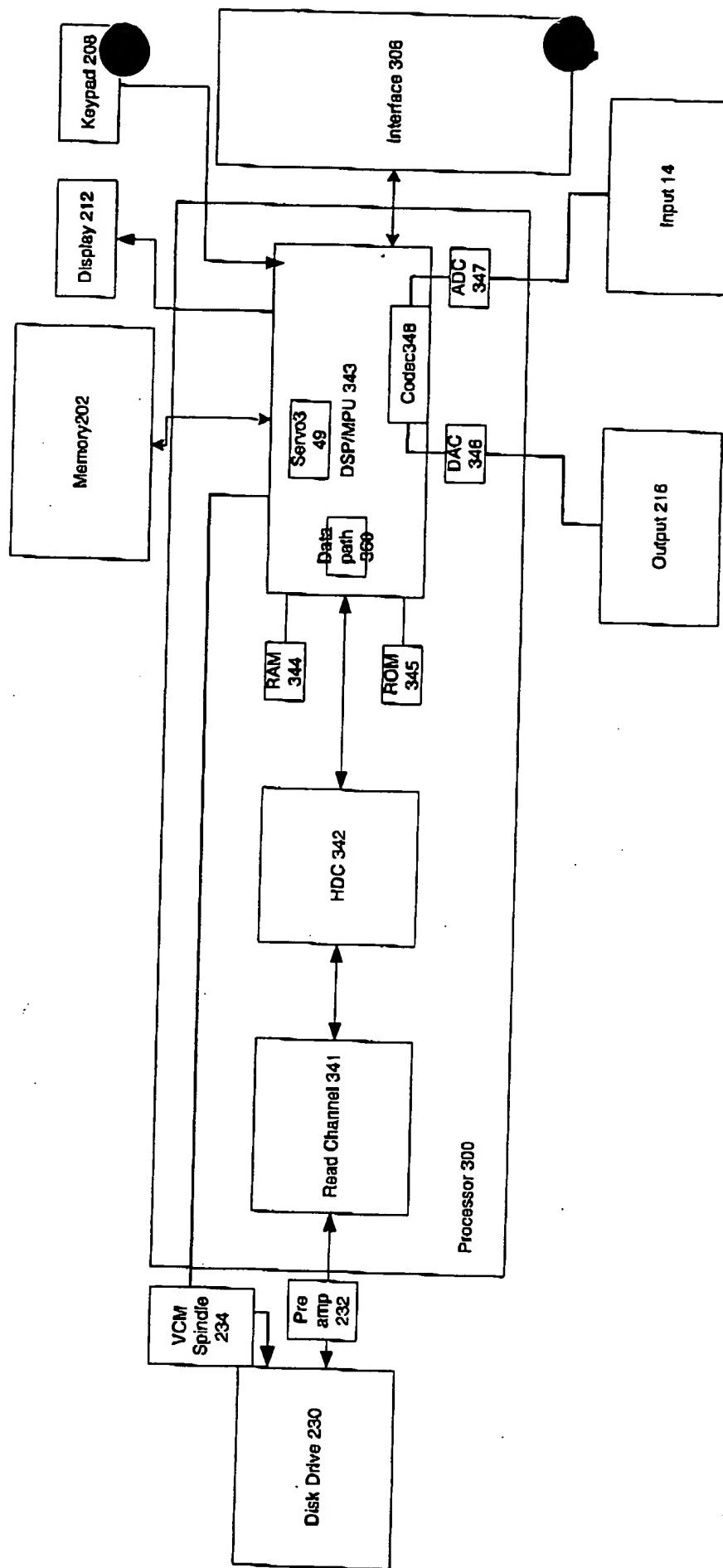


FIG. 3

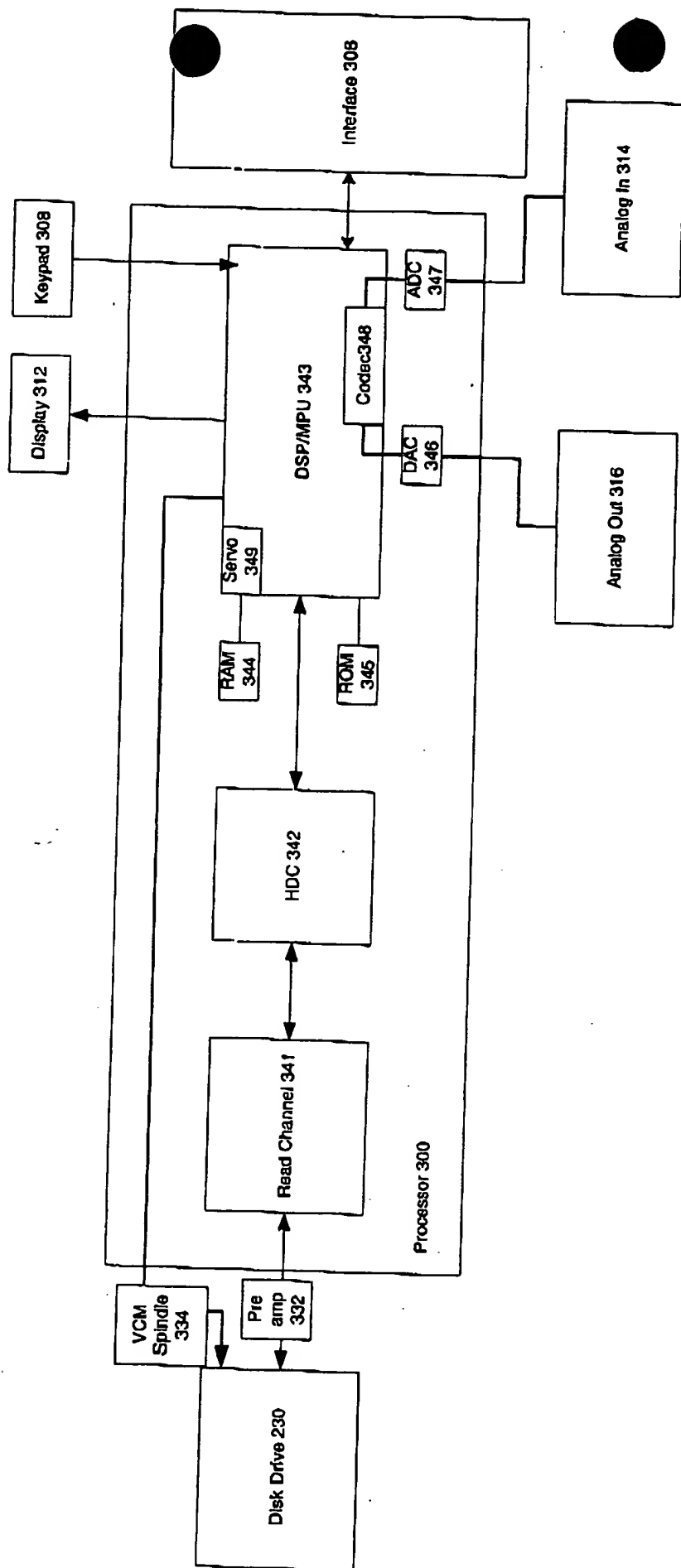
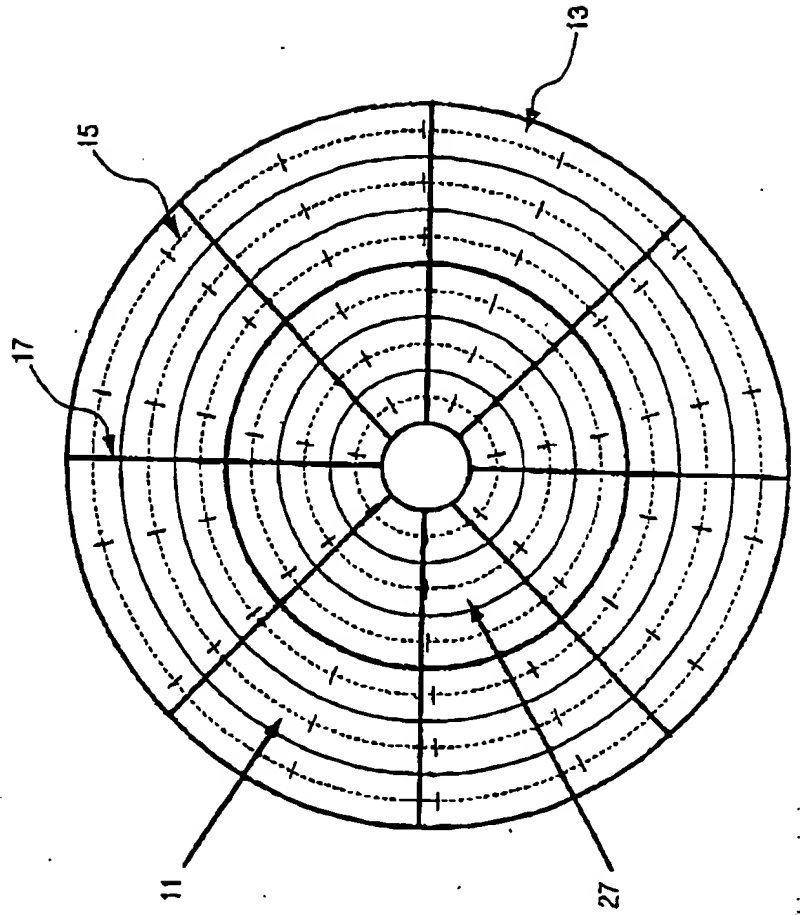


FIG. 5

Fig 6



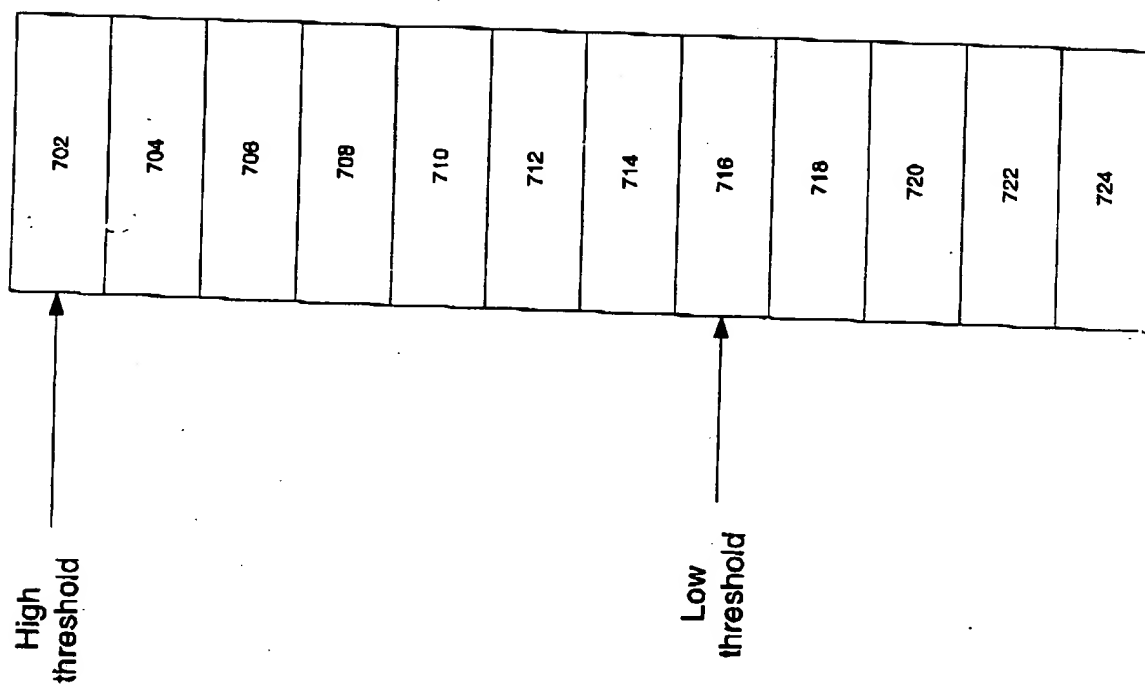


Fig. 7

The diagram illustrates a selection circuit. It features four parallel input paths, each consisting of a selection threshold and a corresponding selection block. The first path has 'selection 1 threshold' and '802 selection 1'. The second path has 'selection 2 threshold' and '804 selection 2'. The third path has 'selection 3 threshold' and '806 selection 3'. The fourth path has 'selection 4 threshold' and '808 selection 4'. All four selection blocks are connected to a single output block labeled '810 selection N'. The output block is connected to a 'selection N threshold'.

Fig. 8

001160-6655560

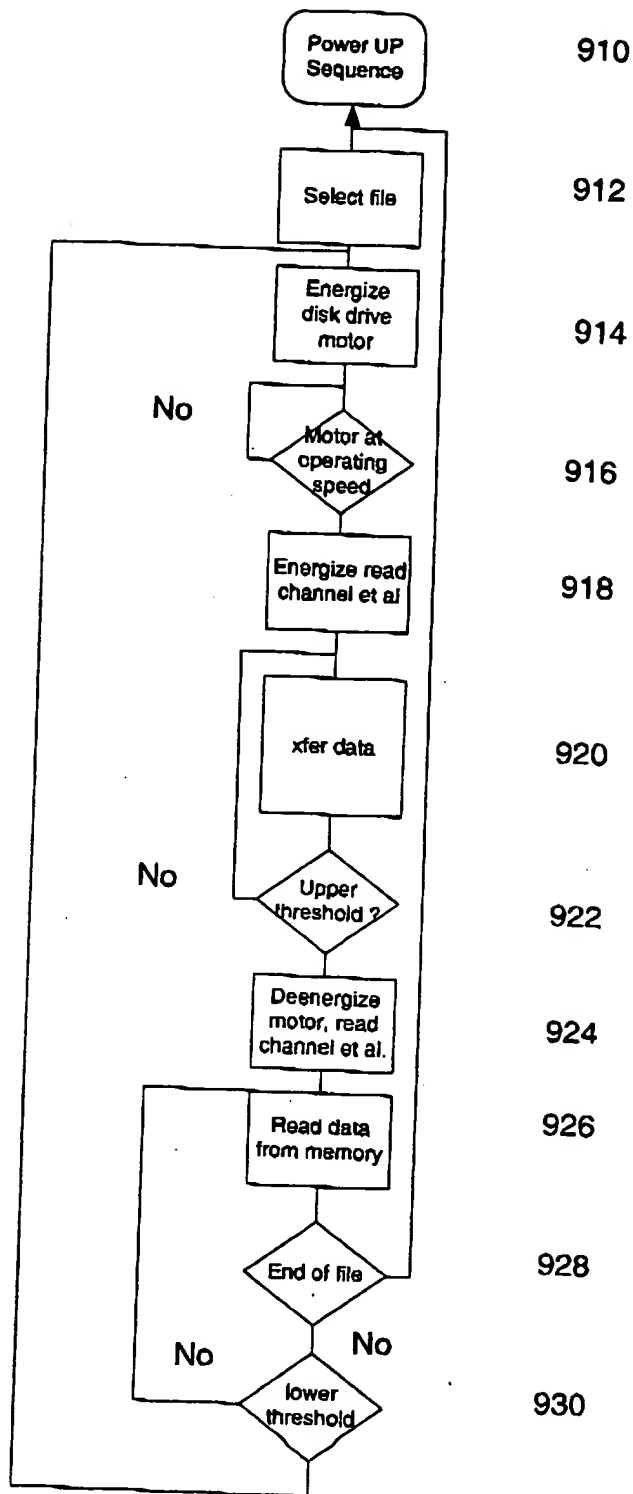


FIG 9

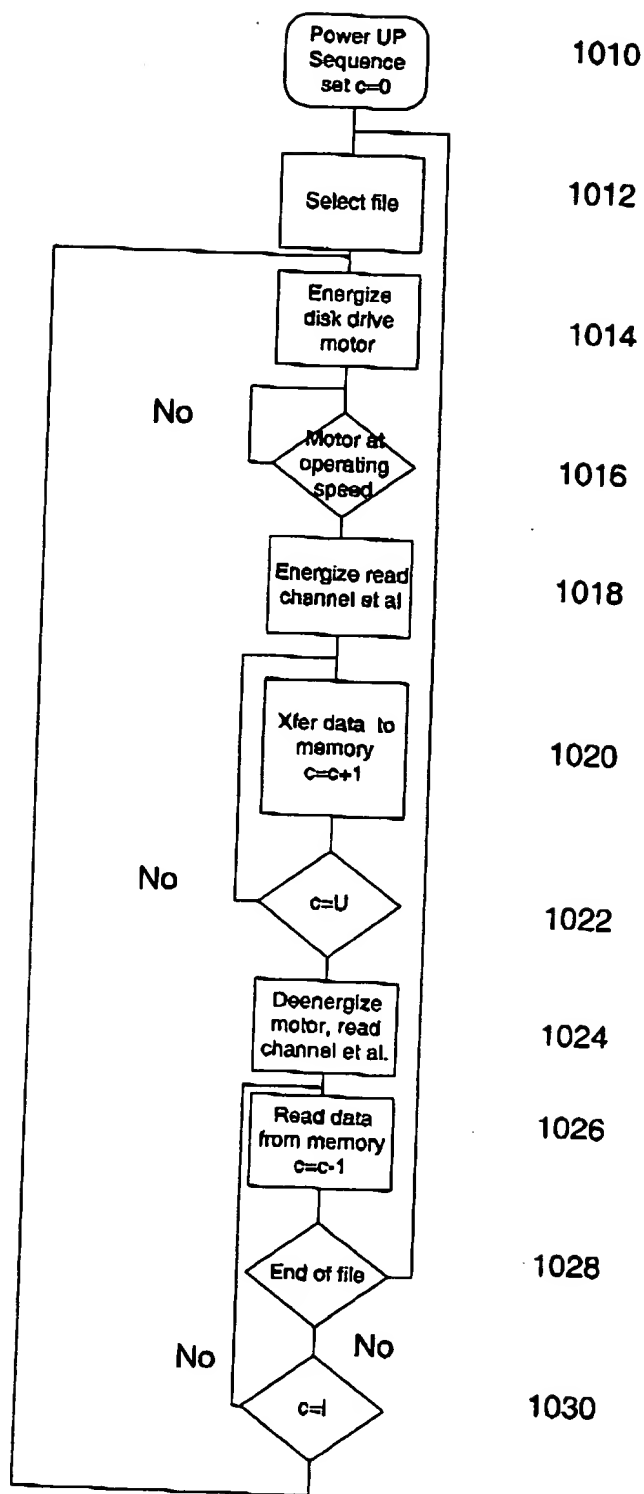


FIG 10

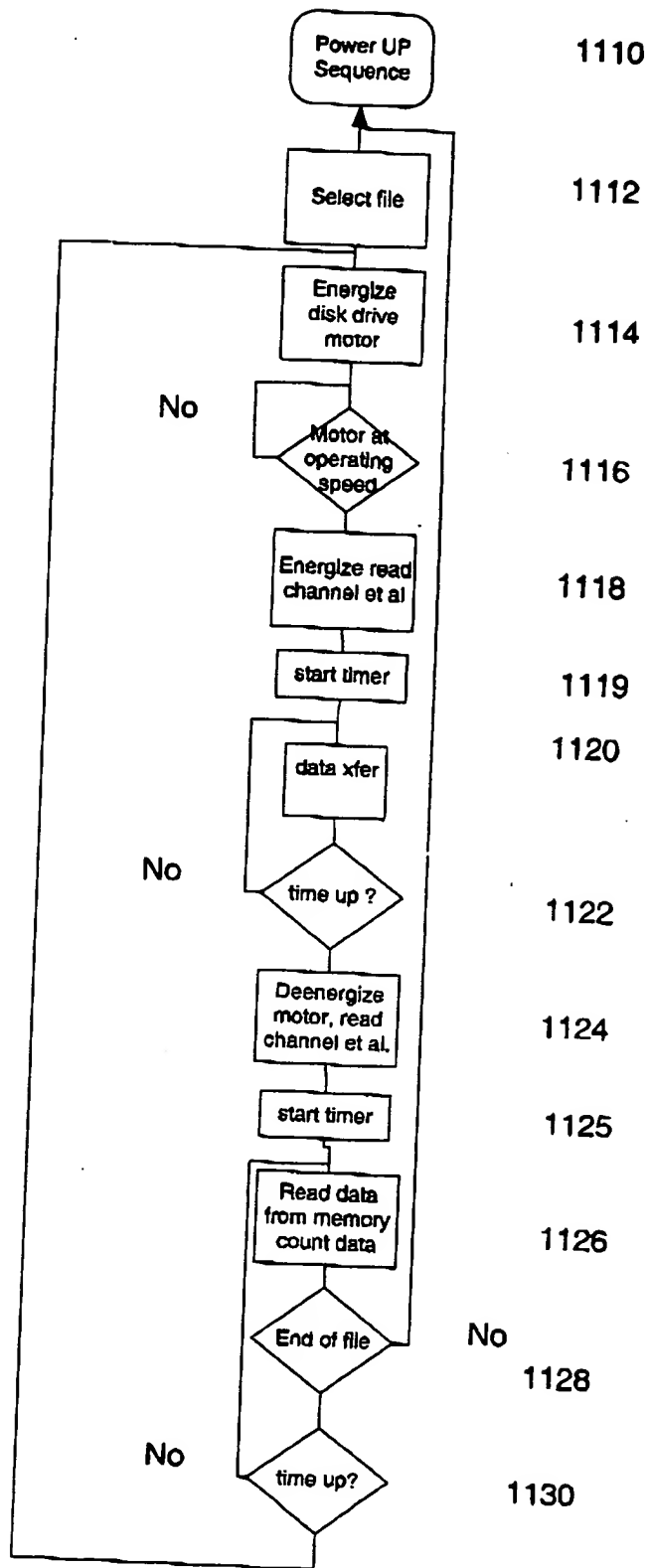


FIG 11

001100-269596

00160-6695960

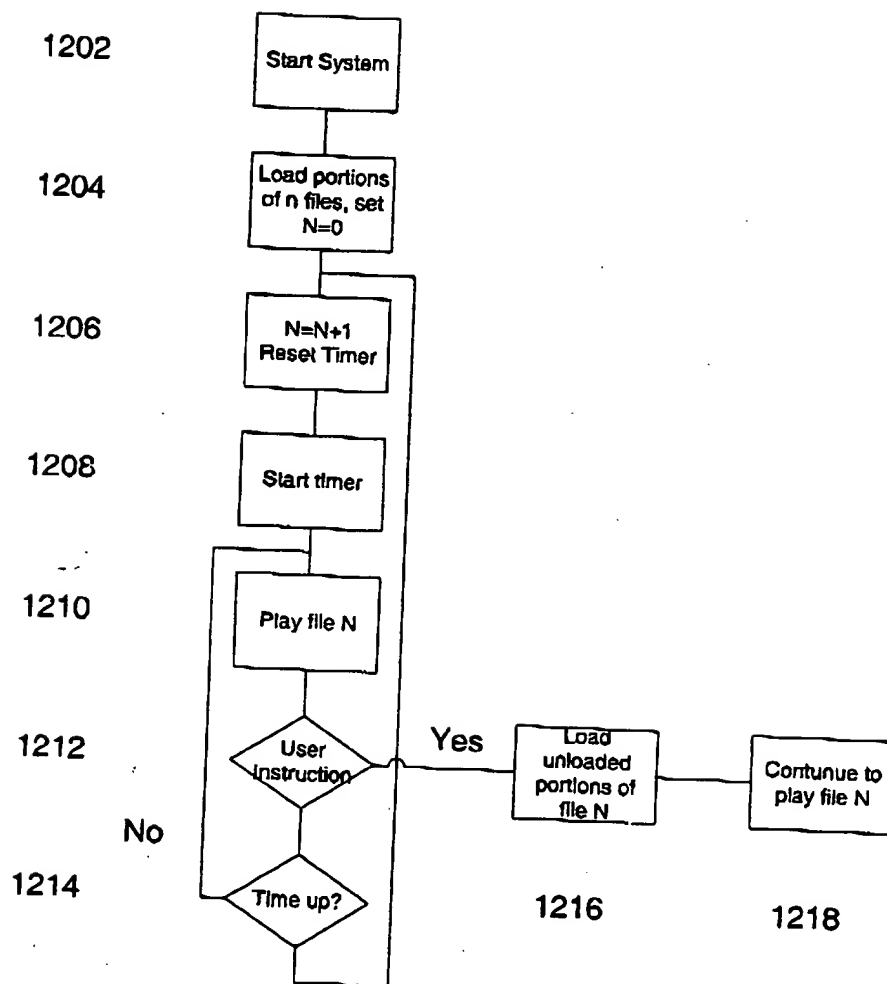


Fig. 12